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04743 7590 05/19/2004

MARSHALL, GERSTEIN & BORUN LLP
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CHICAGO, IL 60606



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Paul B. Stephens	(Depositor's name)
<i>[Signature]</i>	(Signature)
July 9, 2004	(Date)

APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/663,892	09/16/2003	Seon Yong Cha	30205/37980A	4510

TITLE OF INVENTION: MAGNETIC RANDOM ACCESS MEMORY HAVING TRANSISTOR OF VERTICAL STRUCTURE WITH WRITING LINE FORMED ON AN UPPER PORTION OF THE MAGNETIC TUNNEL JUNCTION CELL

APPLN. TYPE	SMALL ENTITY	ISSUE FEE	PUBLICATION FEE	TOTAL FEE(S) DUE	DATE DUE
nonprovisional	NO	\$1330	\$300	\$1630	08/19/2004

EXAMINER	ART UNIT	CLASS-SUBCLASS
TSAL, H JEY	2812	438-003000

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2. For printing on the patent front page, list (1) the names of up to 3 registered patent attorneys or agents OR, alternatively, (2) the name of a single firm (having as a member a registered attorney or agent) and the names of up to 2 registered patent attorneys or agents. If no name is listed, no name will be printed.

1 Marshall,
2 Gerstein &
3 Borun LLP

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(A) NAME OF ASSIGNEE

Hynix Semiconductor Inc.

(B) RESIDENCE: (CITY and STATE OR COUNTRY)

Kyoungki-Do, Republic of Korea

Please check the appropriate assignee category or categories (will not be printed on the patent); ☐ individual ☒ corporation or other private group entity ☐ government

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- ☒ Advance Order - # of Copies 3

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07/14/2004 HDEMSS2 00000051 10663892

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02 FC:1504	300.00 OP
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